

Electronic Acknowledgement Receipt

EFS ID:	10790474
Application Number:	09751659
International Application Number:	
Confirmation Number:	8881
Title of Invention:	PARALLEL DISPATCH WAIT SIGNALING METHOD, METHOD FOR REDUCING CONTENTION OF HIGHLY CONTENTED DISPATCHER LOCK, AND RELATED OPERATING SYSTEMS, MULTIPROCESSOR COMPUTER SYSTEMS AND PRODUCTS
First Named Inventor/Applicant Name:	Brian James Martin
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Filer:	Robert Kevin Perkins/Sandra Kulaga
Filer Authorized By:	Robert Kevin Perkins
Attorney Docket Number:	55,419 (70158)
Receipt Date:	23-AUG-2011
Filing Date:	30-DEC-2000
Time Stamp:	09:42:35
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Change of Address	EMC-00-192_Fee_Address_Indication_Form-08-23-11.pdf	42163 <small>5033330026a0ffba10101308559951743932edf</small>	no	2

Warnings:

Information:

2	Assignee showing of ownership per 37 CFR 3.73(b).	EMC-00-192_Statement_Under _37_CFR_373b-08-23-11.pdf	36598 <small>229e99f0c71e513d0e0147c8ba22001ba21ae6bb</small>	no	1
Warnings:					
Information:					
Total Files Size (in bytes):			78761		
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